

# Temporal Logics for Specification and Verification

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## Transition systems

(Labelled) transition system (TS):  $\mathcal{T} = \langle S, \{R_a\}_{a \in A} \rangle$ , where:

- $S$  is a non-empty set of **states** (program states, control states, configuration states, memory registers, etc.)
- $\{R_a\}_{a \in A}$  is a non-empty set of binary **transition relations** on  $S$ , labeled by a set  $A$  of **actions** (processes).

A transition acts, possibly non-deterministically, on each state from  $S$  and produces its **successor states**.

A transition system may also include various types of designated states, such as **initial, accepting, terminating, deadlock, etc. states**.

All transitions will be assumed **total** (serial), i.e., every state has at least one successor.

Usually, we will consider simple transition systems  $\langle S, R \rangle$  with only one transition relation.

# Transition systems everywhere

**Real (physical) examples:** clocks, vending machines, payphones, semaphores, lifts, etc.

**Abstract examples:** finite state machines (in particular, finite automata), as well as (configuration graphs of) various other abstract computing devices, such as pushdown automata, Turing machines, Petri nets, counter systems, timed automata, etc.

Abstractly, transition systems are just **Kripke frames**.

## Interpreted transition systems

Interpreted transition system (ITS):

$$\mathcal{M} = (\langle S, \{R_a\}_{a \in A} \rangle, L)$$

$\langle S, \{R_a\}_{a \in A} \rangle$  is a TS and the **state description**  $L : S \rightarrow \mathbf{2}^{\text{PROP}}$  is a mapping assigning to each state  $s$  a subset  $L(s)$  of a fixed set PROP of **atomic propositions** that are true at  $s$  – the **label** of  $s$ .

Using special atomic propositions, labels can be used to identify initial, accepting, etc. special states.

Thus, **interpreted transition systems are simply Kripke models**.

Instead of labeling, *valuations*  $V : \text{PROP} \rightarrow \mathbf{2}^S$  are often used in modal logic.

## Paths and computations in transition systems

We will only consider transition systems with serial transition relations, i.e. without terminal (deadlock) states. If necessary, that can be achieved by converting all terminal states to *idle states*.

**Path (run, execution)** in a transition system  $\langle S, R \rangle$  is a sequence

$$s_0, s_1, s_2 \dots,$$

such that  $s_i R s_{i+1}$  for every  $i \in \mathbb{N}$ .

**Computation (trace)** in an interpreted transition system  $\langle S, R, L \rangle$  is the *observable effect of a run*:

$$L(s_0), L(s_1), L(s_2), \dots$$

## Properties of computations: local properties

Local properties: those that refer to immediate successors or predecessors of the current state. Examples:

Some/every immediate successor state satisfies the property  $\varphi$ :

- The system may enable the process  $\tau$  at the next state.
- If the light was red at the previous state and is orange now, it must turn green at the next state.

Some/every immediate predecessor satisfies the property  $\varphi$ .

Typically, these are conditional, e.g.:

- If the process  $\tau$  is currently enabled, the scheduler must have disabled the process  $\rho$  at the previous state.
- If train will be entering the tunnel at the next moment, the semaphore on the other end of the tunnel must be red now.

Local properties can be iterated a fixed number of times, but not indefinitely.

## Universal properties of computations: invariance, safety

**Invariance** properties are properties that *must always hold throughout the computation*, while **safety** properties describe events that *must never happen throughout the computation*.

Examples:

- Safety:
- No deadlock will ever occur. I.e.:
- At least one process will be enabled at any moment of time.
- Not more than one process will ever be in its critical section (e.g., not more than one train will ever be in the tunnel) at the same time.
- The temperature of the reactor must never exceed  $1000^{\circ}\text{C}$ .

Also, **partial correctness properties**:

If a pre-condition  $P$  holds at all initial states, then a post-condition  $Q$  will/must hold at all accepting (terminating) states.

## Existential properties of computations: eventualities, liveness

**Eventuality, liveness** properties: those that *will (must) happen sometime during the computation*. Examples:

- The execution of the program will terminate.
- If the train has entered the tunnel, it will eventually leave it.
- Once a printing job is activated, eventually it will be completed.
- If a message is sent, eventually it will be delivered.

Also, **total correctness properties**:

If a pre-condition  $P$  holds at the initial state, then the computation will reach an accepting (terminating) state, where the post-condition  $Q$  will hold.



## Properties of computations: fairness, precedence

**Fairness properties:** *All processes will be treated 'fairly' by the operating system (scheduler, etc.)*

Examples:

- **Weak fairness:** Every continuous request is eventually granted.
- **Strong fairness:** If a request is repeated infinitely often then it is eventually granted.
- **Impartiality:** Every process is scheduled infinitely often.

**Precedence:**

- The event  $\varphi$  will occur before the event  $\psi$ , which may or may not occur at all.
- If the train has entered the tunnel, it will eventually leave it, before any other train has entered the tunnel.

## Computational tasks in transition systems

- **Local model checking:** given an ITS  $\mathcal{M}$ , state  $s$ , and a state property  $P$ , determine if  $s$  satisfies  $P$ .  
Likewise for path properties.
- **Model satisfiability checking:** given an ITS  $\mathcal{M}$  and a state property  $P$ , determine if there is a state  $s$  in  $\mathcal{M}$  satisfying  $P$ .
- **Global model checking:** given a state property  $P$ , determine the set of states that satisfy  $P$ .
- More general: **Query evaluation:** given a state query, determine the state relation that satisfy it.  
Example of a binary query:  
'The set of all pairs of states  $(u, w)$  such that  $w$  satisfies the property  $P$  and is reachable from  $u$ '.
- **Satisfiability testing:** given a state property  $P$ , determine if there is an ITS  $\mathcal{M}$  and a state  $s$  in  $\mathcal{M}$  satisfying  $P$ .

## Basic modal logic for transition systems

With every type  $\tau = (A, \text{PROP})$  of interpreted transition systems we associate a multimodal language  $\text{TL} = \text{TL}_\tau$  with a set of atomic propositions  $\text{PROP}$  and a family of **modal operators**  $(EX_a)_{a \in A}$ , each representing a transition  $a \in A$ .

Inductive definition of formulae:

$$\varphi = \top \mid p \mid \neg\varphi \mid \varphi \wedge \varphi \mid EX_a\varphi$$

The other logical connectives:  $\perp$ ,  $\rightarrow$ ,  $\vee$ ,  $\leftrightarrow$  are defined as usual. The **dual** of each modal operator  $EX_a$  is defined as  $AX_a := \neg EX_a\neg$ .

Thus,  $EX_a$  corresponds to  $\diamond$  and  $AX_a$  corresponds to  $\square$ .

This is **the basic multi-modal logic** denoted here by  $\text{TL}$ .

$\text{TL}$  is not very expressive. In particular, it can only express **local properties** but no reachability properties; in particular, no safety, eventualities, or fairness.

## Kripke semantics of TL

The semantics of  $\text{TL}_\tau$  is the standard Kripke semantics in interpreted transition systems.

The basic semantic notion of **truth of a formula at a state  $s$  of an interpreted transition system  $\mathcal{M} = (S, \left\{ \overset{a}{\mapsto} \right\}_{a \in A}, L)$**  is defined inductively as follows.

- $\mathcal{M}, s \models \top$ ;
- $\mathcal{M}, s \models p$  iff  $p \in L(s)$ ;
- $\mathcal{M}, s \models \neg\varphi$  iff  $\mathcal{M}, s \not\models \varphi$ ;
- $\mathcal{M}, \pi \models \varphi \wedge \psi$  if  $\mathcal{M}, \pi \models \varphi$  and  $\mathcal{M}, \pi \models \psi$ ;
- $\mathcal{M}, s \models \text{EX}_a\varphi$  if  $\mathcal{M}, t \models \varphi$  for some  $t \in S$  such that  $s \overset{a}{\mapsto} t$ .

The derived truth definition of  $\text{AX}_a$  becomes:

- $\mathcal{M}, s \models \text{AX}_a\varphi$  if  $\mathcal{M}, t \models \varphi$  for every  $t \in S$  such that  $s \overset{a}{\mapsto} t$ .

## Truth, validity, satisfiability

Given an ITS  $\mathcal{M}$ , state  $r$  in  $\mathcal{M}$ , and a formula  $\varphi$  of TL, we say that  $\varphi$  is:

- **satisfied at  $r$  in  $\mathcal{M}$**  if  $\mathcal{M}, r \models \varphi$ .  
We then also say that  **$(\mathcal{M}, r)$  is a model of  $\varphi$** .
- **satisfiable**, if it is satisfied in some ITS.
- **valid in  $\mathcal{M}$** , denoted  $\mathcal{M} \models \varphi$ , if  $\mathcal{M}, s \models \varphi$  for every state  $s \in \mathcal{M}$ .
- **valid**, denoted  $\models \varphi$ , if it is valid in every ITS.

# The linear time temporal logic LTL: introduction

- First introduced and studied by Gabbay, Pnueli, Shelah and Stavi in 1980.
- Extends propositional logic with temporal operators for discrete linear time.
- Used to reason about properties of single computations.

## Temporal operators in LTL: Nexttime

Whereas  $\varphi$  states a property of the current state,  $X\varphi$  states that the next state satisfies  $\varphi$ .

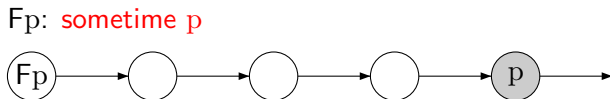
$Xp$ : nexttime p



Thus,  $\varphi \vee X\varphi$  states that  $\varphi$  is satisfied now or at the next state.

## Temporal operators in LTL: Sometime

$F\varphi$  claims that some future (or possibly, the current) state satisfies  $\varphi$  without specifying which one, i.e., that “ $\varphi$  will be true sometime in the future”.



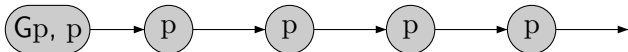
For instance,  $\text{alert} \rightarrow F \text{halt}$  means that if the system currently is in a state of alert, then it will sometime later be in a halt state.



## Temporal operators in LTL: Always

$G\varphi$  claims that all the future states (including the current one) satisfy  $\varphi$ , i.e., that “ $\varphi$  will always be true”.

$Gp$ : always  $p$



Thus,  $G(\text{alert} \Rightarrow F \text{halt})$  means that whenever in the future the system is in a state of alert, it will sometime later be in a halt state.

$G$  is the dual of  $F$ :  $G\varphi$  and  $\neg F\neg\varphi$  are equivalent.

## Temporal operators in LTL: **Until**

The binary operator  $U$  is more expressive than the operator  $F$ .

The formula  $\varphi_1 U \varphi_2$  states that  $\varphi_1$  is true until  $\varphi_2$  is true.

More precisely:  $\varphi_2$  will be true at some future state, and  $\varphi_1$  will hold in the meantime.

$pUq$ : **p until q**



The example  $G(\text{alert} \rightarrow F \text{halt})$  can be refined with the statement that “starting from a state of alert, the alarm remains activated until the halt state is eventually reached”:

$G(\text{alert} \rightarrow (\text{alarm } U \text{halt}))$ .

Note that the operator  $F$  is a special case of  $U$ :  $F\varphi$  and  $\text{true}U\varphi$  are equivalent.

## Temporal operators in LTL: Other operators

**Weak until:  $W$ .** Intuitively, the statement  $\varphi_1 W \varphi_2$  still expresses “ $\varphi_1$  until  $\varphi_2$ ”, but without the inevitable occurrence of  $\varphi_2$ ; if  $\varphi_2$  never occurs, then  $\varphi_1$  must remain true forever.

Thus,  $\varphi_1 W \varphi_2$  is equivalent to  $G\varphi_1 \vee (\varphi_1 U \varphi_2)$ .

**Release:  $R$ .** Defined as the dual of  $U$ , i.e.,  $\varphi_1 R \varphi_2 := \neg(\neg\varphi_1 U \neg\varphi_2)$ .

$\varphi_1 R \varphi_2$  intuitively states that the truth of  $\varphi_1$  releases the requirement for the truth of  $\varphi_2$ .

Exercise: determine the precise meaning of  $R$ .

More precisely, it means that, unless  $\varphi_2$  is true in all future states,  $\varphi_1$  must be true sometime in the future and  $\varphi_2$  must hold between the current state and that future state.

## Formulae and syntax of LTL

LTL formulae:

$$\varphi ::= \underbrace{\perp \mid p \mid \neg\varphi \mid \varphi \wedge \psi}_{\text{propositional logic}} \mid \underbrace{X\varphi \mid F\varphi \mid G\varphi \mid \varphi U \psi}_{\text{temporal extension}}$$

where  $p$  ranges over a countable set of **atomic propositions**  
 $AP = \{p_0, p_1, \dots\}$ , obtained by abstracting properties.

Since a formula only contains finitely many atomic propositions, whenever suitable we can assume that  $AP$  is finite.

$F$  and  $G$  can be regarded as definable in terms of  $U$ .

Some abbreviations:

$$\varphi_1 \rightarrow \varphi_2 := \neg\varphi_1 \vee \varphi_2,$$

$$F^\infty\varphi := GF\varphi \text{ ("}\varphi \text{ holds infinitely often")}.$$

$$G^\infty\varphi := FG\varphi \text{ ("}\varphi \text{ holds eventually")}.$$

$$\varphi_1 R \varphi_2 := \neg(\neg\varphi_1 U \neg\varphi_2).$$

# Expressing properties of computations with LTL

(safety)  $\text{init} \rightarrow G \text{ safe}$ ,

(liveness)  $G(p \rightarrow Fq)$ ,

(total correctness)  $(\text{init} \wedge p) \rightarrow F(\text{end} \wedge q)$ ,

(strong fairness)  $GF \text{ enabled} \rightarrow GF \text{ executed}$ .

## Expressing properties with LTL: exercises

*“Every occurrence of  $p$  will be followed immediately by an occurrence of  $q$  which will hold true until  $p$  ceases to be true.”*

$$G(p \rightarrow X(q \wedge qU\neg p)).$$

*“Every time when a message is sent, it will not be marked as ‘sent’ before an acknowledgment of receipt is returned.”*

$$G(\text{Sent} \rightarrow (\neg\text{MarkedSent} \cup \text{AckReturned})).$$

*“Between every two green signals there will be a red signal”*

$$G(\text{Green} \rightarrow X(\neg\text{Green} \cup \text{Red}))?$$

or:

$$G(\text{Green} \rightarrow X(\neg\text{Green} \text{ W } \text{Red}))?$$

or:

$$G(\text{Green} \wedge F\text{Green} \rightarrow X(\neg\text{Green} \cup \text{Red}))?$$

## Semantics of LTL

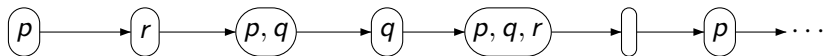
Given an LTL-model  $\sigma$ , a position  $i \in \mathbb{N}$ , and a formula  $\varphi$ , we define inductively the satisfaction relation  $\models$  as follows:

- $\sigma, i \not\models \perp$ ,
- $\sigma, i \models p$  iff  $p \in \sigma(i)$ , for every  $p \in \text{PROP}$ ,
- $\sigma, i \models \neg\varphi$  iff  $\sigma, i \not\models \varphi$ ,
- $\sigma, i \models \varphi_1 \wedge \varphi_2$  iff  $\sigma, i \models \varphi_1$  and  $\sigma, i \models \varphi_2$ ,
- $\sigma, i \models X\varphi$  iff  $\sigma, i + 1 \models \varphi$ ,
- $\sigma, i \models F\varphi$  iff there is  $j \geq i$  such that  $\sigma, j \models \varphi$ ,
- $\sigma, i \models G\varphi$  iff for all  $j \geq i$ , we have  $\sigma, j \models \varphi$ ,
- $\sigma, i \models \varphi_1 U \varphi_2$  iff there is  $j \geq i$  such that  $\sigma, j \models \varphi_2$  and  $\sigma, k \models \varphi_1$  for all  $i \leq k < j$ .

We write  $\sigma \models \varphi$  instead of  $\sigma, 0 \models \varphi$ .

An LTL-formula  $\varphi$  is **satisfiable** if there some LTL-model  $\sigma$  such that  $\sigma \models \varphi$ ;  $\varphi$  is **valid** if  $\neg\varphi$  is not satisfiable.

## Semantics of LTL: exercises



The valuation defining the model  $\sigma$ :

$$V(p) = \{s_{2k} \mid k \in \mathbb{N}\},$$

$$V(q) = \{s_k \mid 2 \leq k \leq 4 \text{ or } 100 \leq k\},$$

$$V(r) = \{s_{3k+1} \mid k \in \mathbb{N}\}.$$

Determine:

$$\sigma, 0 \stackrel{?}{\models} F(q \wedge XX\neg p) \text{ Yes}; \quad \sigma, 0 \stackrel{?}{\models} \neg q \text{ U } (qUr) \text{ Yes};$$

$$\sigma \stackrel{?}{\models} FG\neg(p \wedge q) \text{ No}; \quad \sigma \stackrel{?}{\models} GF\neg(p \wedge q) \text{ Yes};$$

$$\sigma \stackrel{?}{\models} FGF(p \wedge q \wedge r) \text{ Yes}; \quad \sigma \stackrel{?}{\models} G(p \rightarrow X\neg p) \text{ Yes};$$

$$\sigma, 1 \stackrel{?}{\models} F(q \text{ U } \neg(p \vee q \vee r)) \text{ Yes}; \quad \sigma \stackrel{?}{\models} GF((p \wedge \neg r)Ur) \text{ Yes};$$

$$\sigma \stackrel{?}{\models} GF(p \wedge Gq \wedge Xr); \quad \sigma \stackrel{?}{\models} GF(r \text{ U } X(\neg p \wedge Xr)).$$



## Some useful validities of LTL formulae

- $G^\infty \varphi \rightarrow F^\infty \varphi$
- $G\varphi \wedge F\psi \rightarrow \varphi U \psi$
- $\varphi \wedge G(\varphi \rightarrow X\varphi) \rightarrow G\varphi$
- $\varphi \wedge G(\varphi \rightarrow XF\varphi) \rightarrow GF\varphi$
- $F\varphi \leftrightarrow \varphi \vee XF\varphi$
- $G\varphi \leftrightarrow \varphi \wedge XG\varphi$
- $\varphi U \psi \leftrightarrow (\psi \vee (\varphi \wedge X(\varphi U \psi)))$

## Truth of LTL-formulae in interpreted transition systems

Every rooted ITS generates a set of linear LTL-models, viz. all computations starting from the root.

So, LTL formulae can be evaluated in (rooted) ITS  $(\mathcal{M}, s)$ :

$(\mathcal{M}, s) \models \varphi$  iff  $\sigma \models \varphi$  for all computations  $\sigma$  starting at  $s$ .

So, we define **universal** truth of  $\varphi$  at  $(\mathcal{M}, s)$ , denoted  $\mathcal{M}, s \models_{\forall} \varphi$ .

Likewise, we define **existential** truth of  $\varphi$  at  $(\mathcal{M}, s)$ , denoted  $\mathcal{M}, s \models_{\exists} \varphi$ .

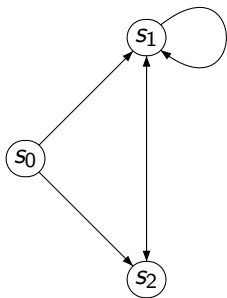
An ITS  $\mathcal{M} = (S, R, L)$  is **finite** if each of  $S$ , the image of  $L$ , and each set in that image are finite sets.

A finite ITS can generate an uncountable set of computations.

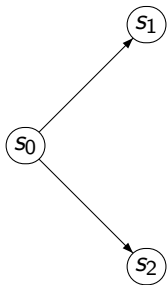
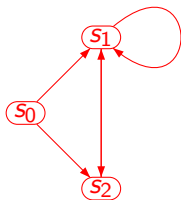
## Unfolding of a transition system, intuitively

- Unfolding of a rooted transition system  $(\mathcal{T}, s)$  starts from the root  $s$ , produces new copies of all successors of that state, and inserts respective transitions to these copies.
- Then the procedure continues recursively from each of these successor states.
- The result is a (usually infinite) tree in which all paths starting from the root of the original TS are presented explicitly as branches.

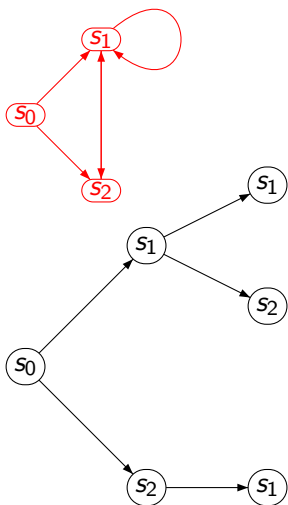
# Unfolding of a rooted transition system 0



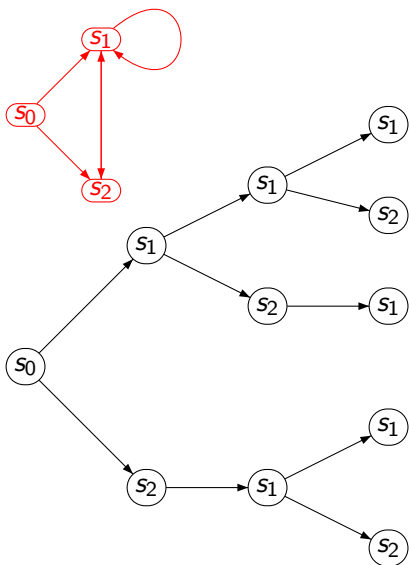
# Unfolding of a rooted transition system 1



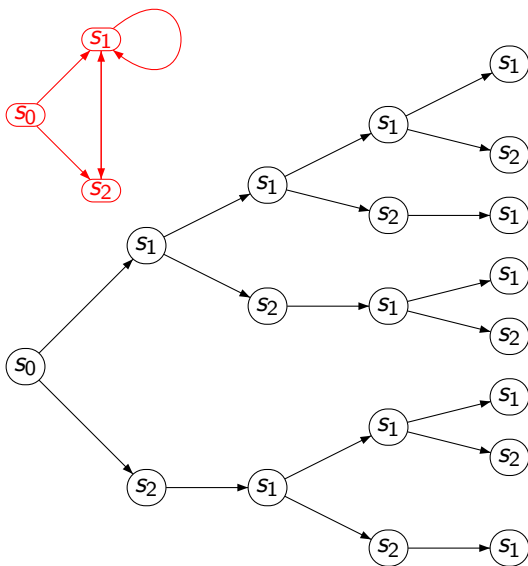
## Unfolding of a rooted transition system 2



# Unfolding of a rooted transition system 3

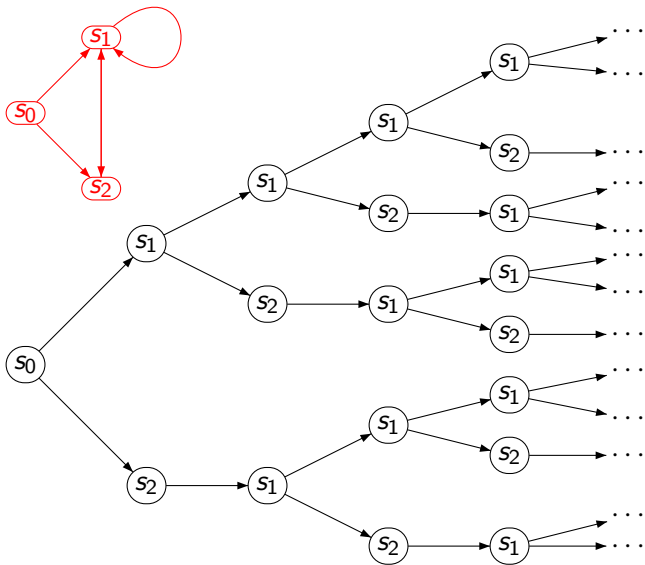


# Unfolding of a rooted transition system 4





# Unfolding of a rooted transition system 5



## Preservation of truth of LTL-formulae in unfoldings

Let  $\widehat{\mathcal{M}}, \widehat{s}$  be the unfolding of the ITS  $\mathcal{M}$  from a state  $s$  in it. Then, paths and computations in  $\mathcal{M}$  starting from  $s$  and in  $\widehat{\mathcal{M}}$  starting from  $\widehat{s}$  are in one-to-one correspondence, naturally mapping  $\widehat{\mathcal{M}}$  onto  $\mathcal{M}$ : the last state of a path in  $\widehat{\mathcal{M}}$  is actually the corresponding path in  $\mathcal{M}$ .

Consequently, for every LTL-formula  $\varphi$ :

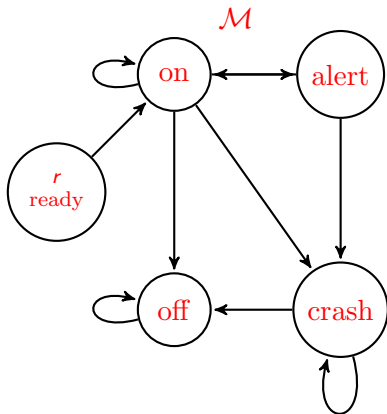
$$\mathcal{M}, s \models_{\exists} \varphi \text{ iff } \widehat{\mathcal{M}}, \widehat{s} \models_{\exists} \varphi$$

and likewise,

$$\mathcal{M}, s \models_{\forall} \varphi \text{ iff } \widehat{\mathcal{M}}, \widehat{s} \models_{\forall} \varphi.$$

The unfolding  $(\widehat{\mathcal{M}}, \widehat{s})$  is also called the **computation tree** of  $(\mathcal{M}, s)$ .

# Model checking of LTL-formulae in ITS: exercises



Check the following:

$\mathcal{M}, r \models_{\exists} G \neg \text{crash}$  ? Yes

$\mathcal{M}, r \models_{\forall} G^{\infty} \neg \text{crash}$  ? No

$\mathcal{M}, r \models_{\exists} F^{\infty} \text{alert} \wedge G^{\infty} \text{off}$  ?

$\mathcal{M}, r \models_{\forall} G^{\infty} (\text{alert} \rightarrow F \text{crash})$  ?

$\mathcal{M}, r \models_{\forall} F^{\infty} (\text{crash} \rightarrow X \text{off})$  ?

$\mathcal{M}, r \models_{\exists} F(\text{on} U \text{off})$  ?

$\mathcal{M}, r \models_{\forall} F^{\infty} (\neg \text{on} U \text{off})$  ?

## Ultimately periodic linear models for LTL

Ultimately periodic linear LTL-model, intuitively: like a lasso, consisting of a finite 'tail', followed by a finite 'loop'.

Formlly, a linear LTL-model  $\sigma : \mathbb{N} \rightarrow \mathcal{P}(\text{PROP})$  is **ultimately periodic (UP)** if there exist natural numbers  $i$  and  $p > 0$  such that  $\sigma(k) = \sigma(k + p)$  for every  $k \geq i$ .

The (possibly empty) sequence  $\sigma(0), \dots, \sigma(i - 1)$  is the **prefix** and  $\sigma(i), \dots, \sigma(i + p - 1)$  is the **loop** of the model.

We say that  $\sigma$  has **prefix index**  $i$  and **period**  $p$ .

Thus, an UP-model  $\sigma$  can be equivalently represented by the finite sequence  $\Gamma_0, \dots, \Gamma_{i+p}$  such that each  $\Gamma_j = \sigma(j)$ .

## LTL model-checking as an algorithmic problem

In order to define the *algorithmic problem* of model-checking, models must be finitely presentable objects.

We say that  $\mathcal{M} = (S, R, L)$  is **finite** whenever  $S$ , the image of  $L$ , and each set in that image are finite sets.

We only consider the algorithmic model-checking problem for LTL in finite ITS.

For instance, UP-models over finite sets of variables are not finite, but can be presented by finite ITS.

## Path model-checking of LTL formulae

Model-checking for LTL in finite ITS is usually intractable.

If we restrict attention to finite UP-models, we have the **path model-checking problem** for LTL:

**input:** A finite UP-model  $\sigma$  and an LTL formula  $\varphi$ .

**question:** Does  $\sigma \models \varphi$ ?

### Proposition

*The path model-checking problem for LTL is decidable in PTIME.*

Open question:

**Is the path model-checking problem for LTL PTime-hard?**

## The full model-checking problems for LTL

Given a finite ITS  $\mathcal{M} = (S, R, L)$ , its **size**  $|\mathcal{M}|$  is the sum

$$\text{card}(S) + \text{card}(R) + \sum_{s \in S} \text{card}(L(s)).$$

The **(universal) local model-checking problem for LTL**:

**input:** an LTL formula  $\varphi$ , a finite ITS  $\mathcal{M}$  and a state  $s \in S$ ,

**question:** Is it the case that  $\mathcal{M}, s \models_{\forall} \varphi$ ?

In the above the codomain of  $L$  is restricted to  $\text{PROP}(\varphi)$ .

**Existential local model-checking**, denoted by  $\text{MC}^{\exists}(\text{LTL})$ , is defined likewise.

## Ultimately periodic model property for satisfiability of LTL-formulae

### Theorem (Sistla& Clarke'85)

*For every satisfiable LTL formula  $\varphi$ , there is an ultimately periodic model  $\sigma$  such that  $\sigma \models \varphi$ , its period is bounded by  $|\varphi| \times 2^{4|\varphi|}$  and its prefix index is bounded by  $2^{4|\varphi|}$ .*



## Complexity of LTL

### Theorem (Sistla& Clarke'85)

*LTL satisfiability is in PSPACE.*

**Proof sketch:** Guessing a sequence of length at most  $2^{4|\varphi|} + |\varphi| \times 2^{4|\varphi|}$  and checking on-the-fly that it is a small satisfiability witness can be done in nondeterministic polynomial space. By Savitch's Theorem this gives a polynomial space upper bound.

### Theorem (Sistla& Clarke'85)

*The existential and universal model-checking problems for LTL are in PSPACE.*

**Proof idea:** By a similar argument, as it suffices to consider only the ultimately periodic computations starting from the given state. The only essential difference is that the maximal values for the prefix index and the period have an additional factor, viz., the number of states of the transition system.

## Branching time temporal logics: intro

While **LTL** is suited for reasoning about single computations in a transition system, branching time logics are intended to reason about the *entire* transition system.

With formulae of branching time logics one can refer to, and quantify over, **all possible computations** starting from a given state.

The study and use of branching-time logics in computer science began in late 1970's - early 1980's, when several branching-time logics were proposed by Abrahamson, Lamport, Ben-Ari, Manna and Pnueli, Clarke and Emerson, Emerson and Halpern, etc.

Soon, a debate on the pros and cons of linear time vs branching time logics ensued.

## The full computation tree logic CTL\*

In response to the debate on pros and cons of linear time vs branching time logics, Emerson and Halpern introduced in 1983 the very expressive logic CTL\*.

The language of CTL\* extends the one of LTL with the **path quantifier** A, where the formula  $A\varphi$  means “ $\varphi$  is true on every computation passing through the current state”.

The set of formulae of CTL\* is defined recursively as follows:

$$\varphi := \perp \mid p \mid \neg\varphi \mid \varphi \wedge \varphi \mid X\varphi \mid \varphi U \varphi \mid A\varphi$$

The other logical connectives  $\perp$ ,  $\rightarrow$ ,  $\vee$ ,  $\leftrightarrow$ , and the temporal operators F and G are definable as usual.

Macros:  $F^\infty := GF$  and  $G^\infty := FG$ .

Existential path quantifier:  $E\varphi := \neg A\neg\varphi$ .

## State and path formulae in CTL\*

Two types of CTL\*-formulae: **state formulae** StateFor and **path formulae** PathFor, defined by mutual induction:

StateFor:

- All atomic propositions and  $\perp$  are in StateFor.
- If  $\varphi, \psi \in \text{StateFor}$  then  $\neg\varphi, \varphi \wedge \psi \in \text{StateFor}$ .
- If  $\varphi \in \text{PathFor}$ , then  $\text{A}\varphi \in \text{StateFor}$ .

PathFor:

- $\text{StateFor} \subset \text{PathFor}$ .
- If  $\varphi, \psi \in \text{PathFor}$  then  $\neg\varphi, \varphi \wedge \psi, \text{X}\varphi, \varphi \text{U}\psi \in \text{PathFor}$ .

## Expressing properties of transition systems with CTL\*

- *Partial correctness along every possible computation:*

$$\varphi \rightarrow \text{AG}(\text{terminal} \rightarrow \psi).$$

- *Partial correctness along some possible computation:*

$$\varphi \rightarrow \text{EG}(\text{terminal} \rightarrow \psi).$$

- *Total correctness along every possible computation:*

$$\varphi \rightarrow \text{AF}(\text{terminal} \wedge \psi).$$

- *and total correctness along some possible computation:*

$$\varphi \rightarrow \text{EF}(\text{terminal} \wedge \psi).$$

- *Fairness along every possible computation:*

$$\text{A}(\text{GF}(\text{Resource requested}) \rightarrow \text{F}(\text{Resource granted}))$$

## Expressing properties with CTL\*: exercises

“On some computation starting from the current state every occurrence of  $p$  is eventually followed by an occurrence of  $q$ ”

$$EG(p \rightarrow Fq)$$

“From every state of every computation on which  $q$  is always true thereafter, a computation starts on which  $p$  will be true infinitely often”.

$$AG(Gq \rightarrow EGFp)$$

“There is no computation on which some state where  $q$  is true starts a computation on which  $p$  is never true.”

$$\neg EF(q \wedge EG\neg p)$$

“On every computation the successor state of some state where  $p$  is true starts a computation on which  $q$  is true until  $p$  becomes false.”

$$AF(p \wedge XE(qU\neg p)).$$

## Formal semantics of CTL\*

CTL\*-models: interpreted transition systems

Let  $\mathcal{M} = \langle S, R, L \rangle$  is an ITS and  $\pi$  is a path in  $\mathcal{M}$ .

$\mathcal{M}, \pi \models \varphi$  means that  $\varphi$  is true on the path  $\pi$  in  $\mathcal{M}$ .

Notation: given a path  $\pi$ , we obtain the path  $\pi_{\geq k}$  by chopping off the first  $k$  states of  $\pi$ , i.e.,  $\pi_{\geq k} = \pi(k), \pi(k+1), \pi(k+2), \dots$

The inductive definition of  $\mathcal{M}, \pi \models \varphi$ :

- $\mathcal{M}, \pi \models p$  iff  $p \in L(\pi(0))$  for  $p \in \text{PROP}$ ;
- $\mathcal{M}, \pi \models \neg\varphi$  iff  $\mathcal{M}, \pi \not\models \varphi$ ;
- $\mathcal{M}, \pi \models \varphi \wedge \psi$  iff  $\mathcal{M}, \pi \models \varphi$  and  $\mathcal{M}, \pi \models \psi$ ;
- $\mathcal{M}, \pi \models X\varphi$  iff  $\mathcal{M}, \pi_{\geq 1} \models \varphi$ ;
- $\mathcal{M}, \pi \models \varphi U \psi$ , iff  $\mathcal{M}, \pi_{\geq j} \models \psi$  for some  $j \geq 0$  and  $\mathcal{M}, \pi_{\geq i} \models \varphi$  for every  $i$  such that  $0 \leq i < j$ .
- $\mathcal{M}, \pi \models A\varphi$  iff  $\mathcal{M}, \pi' \models \varphi$  for every path  $\pi'$  in  $\mathcal{M}$  with the same initial state as  $\pi$ .

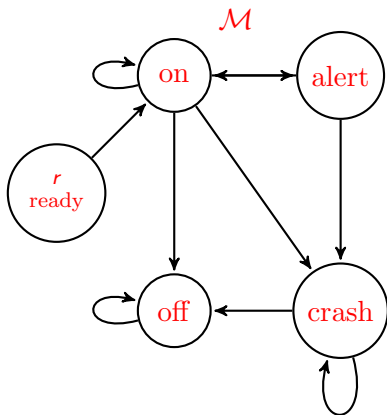
# Preservation of truth of CTL\*-formulae in unfoldings

For every state formula  $\varphi$  of CTL\*, every interpreted transition system  $\mathcal{M}$ , and a state  $s$  in it:

$$\mathcal{M}, s \models \varphi \text{ iff } \widehat{\mathcal{M}}, \widehat{s} \models \varphi$$



# Model checking of CTL\*-formulae in ITS: exercises



Check the following:

$$\mathcal{M}, r \stackrel{?}{\models} EG \neg \text{crash} \quad \text{Yes}$$

$$\mathcal{M}, r \stackrel{?}{\models} AGF \neg \text{crash} \quad \text{No}$$

$$\mathcal{M}, r \stackrel{?}{\models} AGEF \neg \text{crash} \quad \text{Yes}$$

$$\mathcal{M}, r \stackrel{?}{\models} A(GF \text{alert} \rightarrow GF(\text{on} \vee \text{crash}))$$

$$\mathcal{M}, r \stackrel{?}{\models} EG(\text{EX crash} \wedge EG \neg \text{crash})$$

$$\mathcal{M}, r \stackrel{?}{\models} AGEF(\text{on} \text{ U } \text{off})$$

## Truth, validity, satisfiability

Given a CTL\*-model  $\mathcal{M}$ :

- path formula  $\varphi$  is **true of the state  $s$  of  $\mathcal{M}$** , denoted  $\mathcal{M}, s \models \varphi$ , if  $\mathcal{M}, \pi \models \varphi$  for every path  $\pi$  in  $\mathcal{M}$  such that  $\pi(0) = s$ .
- formula  $\varphi$  is **valid in  $\mathcal{M}$** , denoted  $\mathcal{M} \models \varphi$ , if  $\mathcal{M}, s \models \varphi$  for every state  $s$  in  $\mathcal{M}$ .
- formula  $\varphi$  is **valid**, denoted  $\models \varphi$ , if it is valid in every ITS.
- path formula  $\varphi$  is **satisfiable** if it is true of some path  $\pi$  in some interpreted transition system  $\mathcal{M}$ .
- likewise, a state formula  $\varphi$  is **satisfiable** if it is true of some state  $s$  in some interpreted transition system  $\mathcal{M}$ .

Note that validity in CTL\* is not closed under uniform substitutions.

Indeed,  $p \rightarrow Ap$  is valid for any  $p \in \text{PROP}$ , while  $Gp \rightarrow AGp$  is not valid.

## Some useful validities in CTL\*

The following formulae are CTL\*-valid:

- $A\varphi$  for every LTL-valid formula  $\varphi$ ;
- $A\varphi \rightarrow \varphi$ ; (NB:  $\varphi$  can be a path or a state formula.)
- $A(\varphi \rightarrow \psi) \rightarrow (A\varphi \rightarrow A\psi)$ ;
- $AX\varphi \rightarrow XA\varphi$ ;  $AG\varphi \rightarrow GA\varphi$ ;
- $AG(\varphi \rightarrow EX\varphi) \rightarrow (\varphi \rightarrow EG\varphi)$ ;
- $AG\varphi \leftrightarrow \varphi \wedge AXAG\varphi$ ;  $EG\varphi \leftrightarrow \varphi \wedge EXEG\varphi$ ;
- $A(\varphi U \psi) \leftrightarrow \psi \vee AXA(\varphi U \psi)$ ;  $E(\varphi U \psi) \leftrightarrow \psi \vee EXE(\varphi U \psi)$ ;
- $AGEF\varphi \rightarrow EGF\varphi$ ; (Burgess's formula)
- $AG(A\varphi \rightarrow EXFA\varphi) \rightarrow (A\varphi \rightarrow \exists GFA\varphi)$ ;
- $AG(E\varphi \rightarrow EX((E\psi U E\theta))) \rightarrow (E\varphi \rightarrow EG((E\psi U E\theta)))$   
(Reynold's limit closure formula).

## CTL as a fragment of CTL\*

The **computation tree logic** CTL: fragment of CTL\*, introduced in 1980-81, before CTL\*, by Clarke and Emerson.

The language and syntax of CTL are the same as those of CTL\*. Syntactic restriction on the CTL-formulae: **the temporal operators must be immediately quantified by path quantifiers**.

Thus, in CTL there are **only state formulae**.

For instance  $A GF\varphi$  and  $E(F\varphi \wedge \varphi U\psi)$  are not CTL-formulae.  $A(\varphi_1 U\varphi_2)$  and  $E(\varphi_1 U\varphi_2)$  are not inter-definable in CTL, so both path quantifiers must be present in the language.

Recursive definition of CTL-formulae:

$$\varphi ::= \top \mid p \mid \neg\varphi \mid (\varphi_1 \wedge \varphi_2) \mid AX\varphi \mid A(\varphi_1 U\varphi_2) \mid E(\varphi_1 U\varphi_2).$$

The semantics of CTL is the same as CTL\*.

## Some definable operators in CTL

- $EX\varphi := \neg AX\neg\varphi$ ,
- $AF\varphi := A(\top U\varphi)$ ,
- $EFF\varphi := E(\top U\varphi)$ ,
- $AG\varphi := \neg EF\neg\varphi$ ,
- $EG\varphi := \neg AF\neg\varphi$ .

Thus, LTL can be regarded as the fragment of CTL\* consisting of all *purely path formulae*, i.e., those containing no path quantifiers, while CTL is the fragment of CTL\* consisting of all *purely state formulae*.

## Expressiveness and extensions of CTL

For invariance and eventuality properties, CTL is essentially as expressive as CTL\*. However, CTL is not suitable for expressing *fairness properties* where  $G^\infty$  and  $F^\infty$  are essentially used.

(The original version of CTL contained these operators, too.)

E.g., fairness along every possible computation, expressible in CTL\* is beyond the expressiveness of CTL.

But, how about:

$$AG AF(\text{resource requested}) \rightarrow AF(\text{resource granted})?$$

Some extensions of CTL:

- **CTL<sup>2</sup>**: allows pairing of two temporal operators after a path quantifier, thus enabling expression of fairness properties.
- **CTL<sup>+</sup>**: allows any boolean combinations of unnested temporal operators in the scope of a path quantifier. As expressive as CTL, but exponentially more succinct.
- **CTL with past.**

# Model-checking of CTL\*

## reduced to model-checking of LTL

Model-checking of CTL\*-formulae can be reduced to global model-checking of LTL-formulae.

The idea:

- CTL\*-formulae with path quantifier rank 0 are precisely the LTL-formulae.
- For a CTL\*-formula of a positive path quantifier rank replace the maximal state subformulae by fresh propositional variables to obtain an LTL-formula. Compute the extensions of the maximal state subformulae recursively.

This procedure gives a PSPACE-complete method for model-checking of CTL\* (optimal since LTL is a fragment of CTL\*).

## Example of model checking CTL\*-formula

Given: CTL\*-model  $\mathcal{M}$ , a computation  $\sigma$  in it, and the CTL\*-formula  $\alpha = G(A\varphi \rightarrow XEF\varphi) \wedge A\varphi \wedge \neg GEF\varphi$  for some formula  $\varphi$ .

To determine whether  $\mathcal{M}, \sigma \models \alpha$  do the following.

The maximal state subformulae of  $\alpha$  are  $A\varphi$  and  $EF\varphi$ .

Replace these respectively by  $p$  and  $q$  (both non-occurring in  $\varphi$ ) to obtain the LTL-formula  $\alpha' = G(p \rightarrow Xq) \wedge p \wedge \neg Gq$ .

Applying recursively the same procedure, do global model-checking of each of  $A\varphi$  and  $EF\varphi$  to compute  $\|A\varphi\|$  and  $\|EF\varphi\|$  in  $\mathcal{M}$ .

Then, modify  $\mathcal{M}$  into  $\mathcal{M}'$  by re-defining  $L$  to  $L'$ , so that  $\|p\| = \|A\varphi\|$  and  $\|q\| = \|EF\varphi\|$ .

We claim that  $\mathcal{M}, \sigma \models \alpha$  iff  $\mathcal{M}', \sigma \models \alpha'$ .



## Model-checking problems for CTL

The **local CTL model-checking problem LMC(CTL)**:

**input:** a CTL formula  $\varphi$ , a finite ITS  $\mathcal{M}$ , and  $s_0 \in \mathcal{M}$ .

**output:** 1 if  $\mathcal{M}, s_0 \models \varphi$ , 0 otherwise.

The **global model-checking problem GMC(CTL)**:

**input:** a CTL formula  $\varphi$  and a finite ITS  $\mathcal{M}$ .

**output:** the set  $\|\varphi\|_{\mathcal{M}} = \{s \in \mathcal{M} : \mathcal{M}, s \models \varphi\}$ .

## Polynomial time model-checking algorithm for CTL

Unlike CTL\*, model-checking of CTL-formulae can be done very efficiently.

**Proposition** (Clarke and Emerson, 1981)

*Let  $\mathcal{M} = (W, R, L)$  be a CTL model and  $\varphi$  be a CTL formula.  $\|\varphi\|_{\mathcal{M}}$  can be computed in time  $\mathcal{O}((\text{card}(R) + \text{card}(W)) \times |\varphi|)$ .*

## P<sub>TIME</sub> model-checking algorithm for CTL: sketch

Given an ITS  $\mathcal{M}$  and a formula  $\theta$ , the algorithm computes the extension  $\|\theta\|_{\mathcal{M}}$  inductively on the structure of  $\theta$ .

The propositional cases are routine.

$\|\text{EX}\varphi\|$  consists of all states which have a successor in  $\|\varphi\|$ .

$\|\text{AX}\varphi\|$  consists of all states which have all their successors in  $\|\varphi\|$ .

$\|\text{E}\varphi\text{U}\psi\|$  consists of all states from which  $\|\psi\|$  is reachable by a path within  $\|\varphi\|$ . These are computed iteratively, in linear time.

$\|\text{EG}\varphi\|$  consists of all states from which some strongly connected component (SCC) in  $\|\varphi\|$  is reachable by a path within  $\|\varphi\|$ .

Again, these are computed in linear time.

**Exercise:** work out the case  $\|\text{A}\varphi\text{U}\psi\|$ .

**Exercise:** extend the algorithm to  $G^{\infty}$ .

# Decidability and complexity of satisfiability testing for CTL\* and CTL

## Theorem (Clarke & Emerson, 1981)

*The satisfiability problem for CTL is EXPTIME-complete.*

**Proof sketch:** The upper bound is provided by the tableau construction that will be described later.

The matching lower bound is by reduction from alternating polynomial space bounded Turing machines.

## Theorem (Vardi & Stockmeyer, 1985; Emerson & Jutla, 1999)

*The satisfiability problem for CTL\* is 2EXPTIME-complete.*

**Proof sketch:** The lower bound is established by Vardi and Stockmeyer by reduction from alternating exponential space bounded Turing machines.

The upper bound is proved by Emerson and Jutla by an elaborated reduction to non-emptiness of automata on infinite trees.

## Linear-time vs branching-time logics

Debate on pros and cons of linear vs branching time temporal logics in terms of:

- *expressiveness,*
- *complexity of satisfiability,*
- *complexity of model checking,*
- *suitability for specific practical purposes.*

## Linear-time vs branching-time logics: expressiveness

LTL and CTL are expressively incompatible:

1. The CTL-formula  $AF AGp$  is not expressible in LTL.
2. The LTL-formula  $AFGp$  is not expressible in CTL.

### Proof.

Every CTL\* formula  $\varphi$  is associated with the LTL-formula  $LTL(\varphi)$  obtained from  $\varphi$  by deleting all path quantifiers.

Now, for every path  $\pi$  in a CTL\*-model  $\mathcal{M}$ , we have that  $\pi \models LTL(\varphi)$  iff  $\mathcal{M}_\pi, \pi \models \varphi$ , where  $\mathcal{M}_\pi$  is the CTL\*-model obtained by restricting  $\mathcal{M}$  over the path  $\pi$ .

**Exercise:** complete the proof.



CTL\* subsumes both rivals but is computationally expensive.

## Linear-time vs branching-time logics: complexity

CTL\*: satisfiability testing is **deterministic  $2^{\text{EXPTIME}}$ -complete**; model-checking is **PSPACE-complete**. Both are intractable.

CTL: good for model-checking (**bilinear time** complexity) but bad for satisfiability testing (**deterministic EXPTIME-complete**)

LTL: worse for model-checking, but better for satisfiability testing (both **PSPACE-complete**).

## Summary

Each of linear-time and branching-time logics has its pros and cons as a framework for formal specification.

They are generally incompatible and there are no decisive theoretical arguments in favour of one approach to the other.

Vardi: *“The debate might end up being decided by the market-place rather than by the research community”*.